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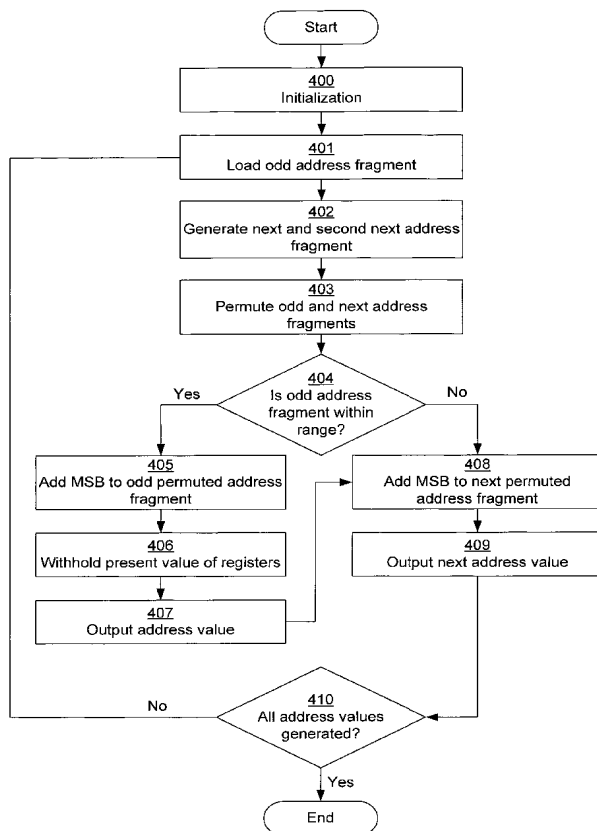
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(54) Title: ADDRESS GENERATOR FOR AN INTERLEAVER MEMORY AND A DEINTERLEAVER MEMORY



(57) Abstract: Method and device for generating an address value for addressing an interleaver memory. Consecutive address fragments to which a most significant bit(s) is to be appended are generated. Only a fraction of the address fragments generated, which potentially will exceed a maximum allowable value, is compared to the maximum allowable value. If the compared address fragment exceeds the maximum allowable value it is discarded. If the compared address fragment does not exceed the maximum allowable value it is accepted.

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